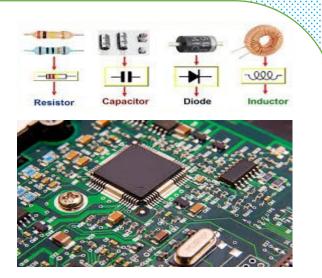


Electronics 1

BSC 113

Fall 2022-2023

Lecture 10



Sinusoidal Steady State Analysis Introduction to Semiconductors

INSTRUCTOR

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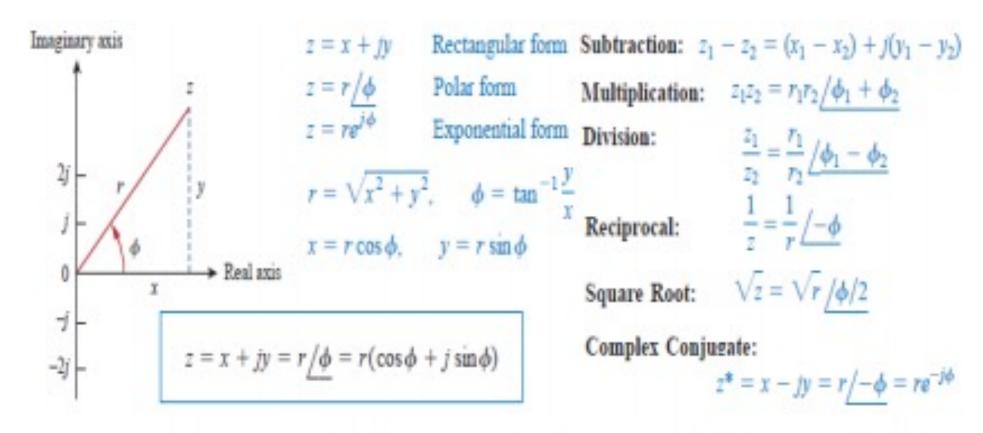
Contents

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- 2) Electrical circuit analysis
- 3) Average and RMS
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- 6) Semiconductor concepts
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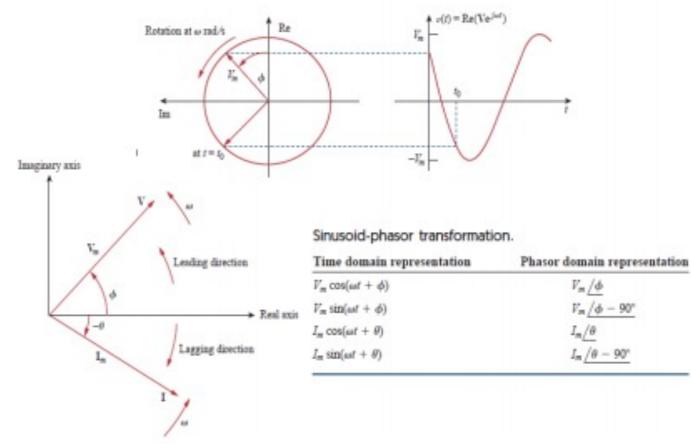
1- Phasors and sin wave

A phasor is a complex number that represents the amplitude and phase of a sinusoid.



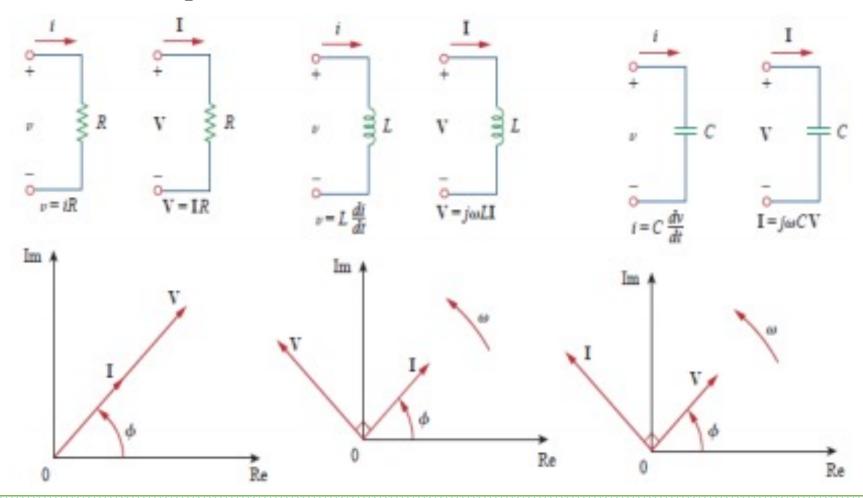
1- Phasors and sin wave

A phasor is a complex number that represents the amplitude and phase of a sinusoid.



2- Electrical circuit analysis

> Phasor relationships of circuit elements



2- Electrical circuit analysis

> Summary of voltage-current relationships

Element	Time domain	Frequency domain
R	v - Rt	V - RI
L	$v = L \frac{di}{dt}$	$V = j\omega LI$
C	$t - C \frac{dv}{dt}$	$V = \frac{I}{\int \omega C}$

3- Average and RMS

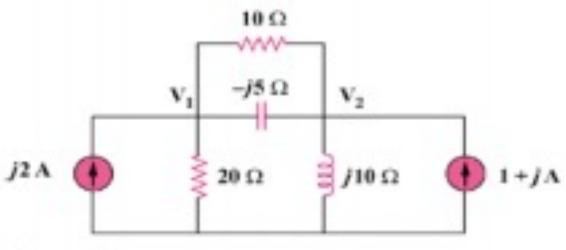
The two important parameter in electronics calculations is average and root mean square value which are discussed here.

$$v_{avg} = \frac{1}{T} \int_0^T v(t) dt$$

> Given a periodic function, its rms value (or the effective value) is given by

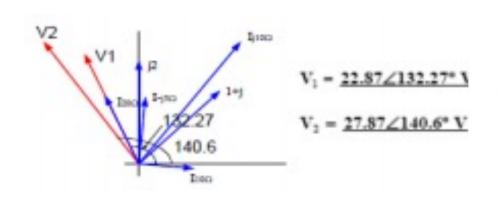
$$v_{rms} = \sqrt{\frac{1}{T} \int_0^T v^2(t) dt}$$

Example



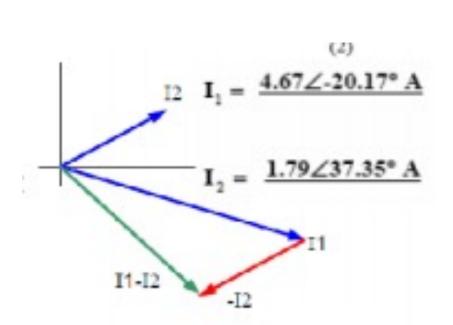
At node 1,

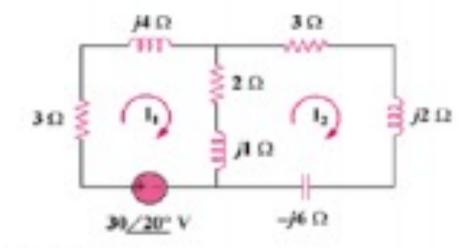
$$j2 = \frac{V_1}{20} + \frac{V_1 - V_2}{10} + \frac{V_1 - V_2}{-j5}$$
$$j40 = (3 + j4) V_1 - (2 + j4) V_2$$



$$\frac{\mathbf{V}_1 - \mathbf{V}_2}{10} + \frac{\mathbf{V}_1 - \mathbf{V}_2}{-j5} + 1 + j = \frac{\mathbf{V}_2}{j10}$$
$$10(1+j) = -(1+j2)\mathbf{V}_1 + (1+j)\mathbf{V}_2$$

Example





For mesh 1,

$$(5+j5)\mathbf{I}_1 - (2+j)\mathbf{I}_2 - 30\angle 20^\circ = 0$$

 $30\angle 20^\circ = (5+j5)\mathbf{I}_1 - (2+j)\mathbf{I}_2$
(1)

For mesh 2,

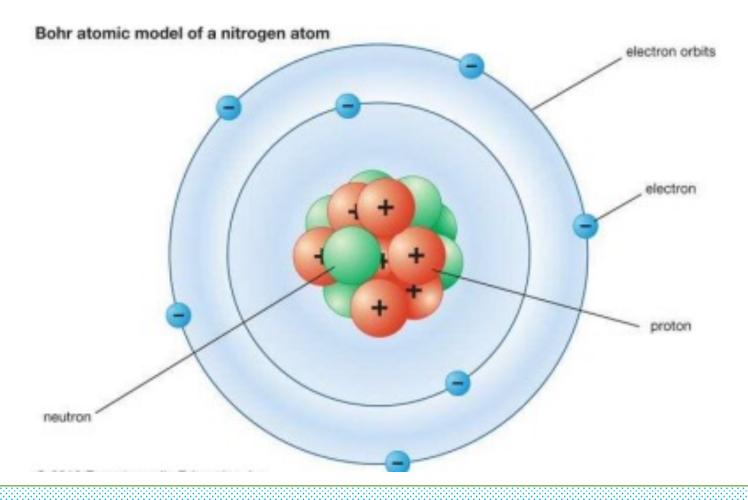
$$(5+j3-j6)\mathbf{I}_2 - (2+j)\mathbf{I}_1 = 0$$

 $0 = -(2+j)\mathbf{I}_1 + (5-j3)\mathbf{I}_2$
(2)

Semiconductor

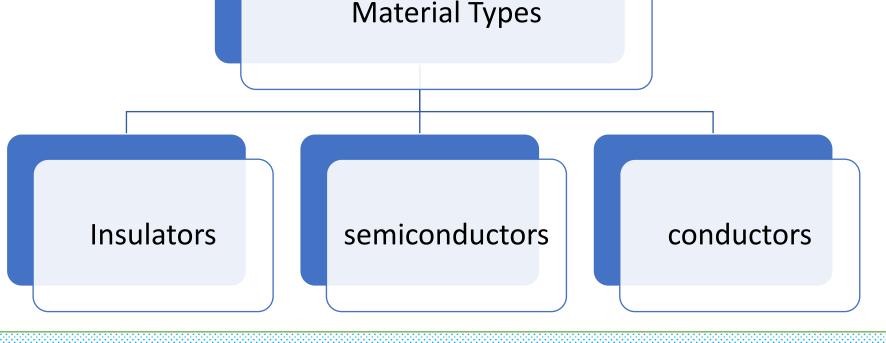
☐ 1. Definitions of atom

> The atom is considered the smallest particle of the element.

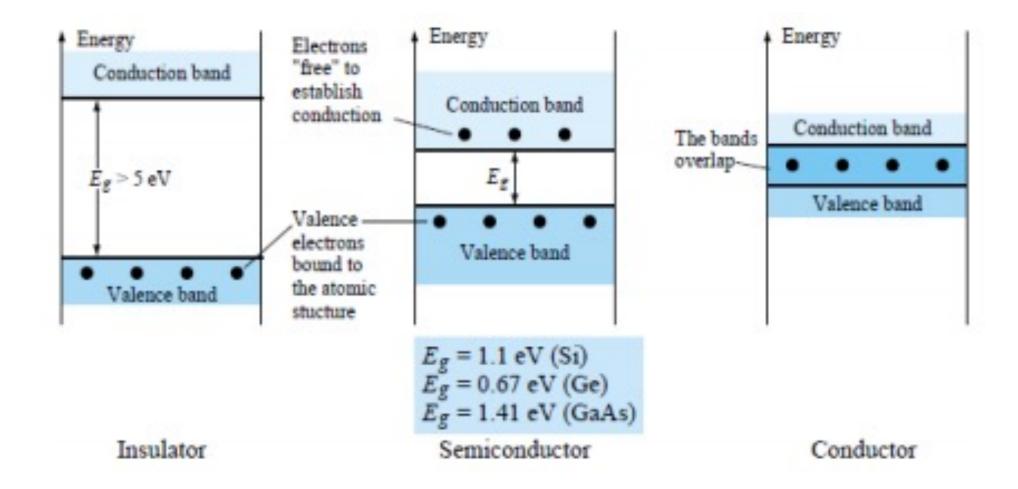


☐ 2. Basic of materials

➤ 2.1 Resistivity: We can define the resistivity as resistance of matter against flow the electrical current. Now the differences between materials will be stated in the following subsection.

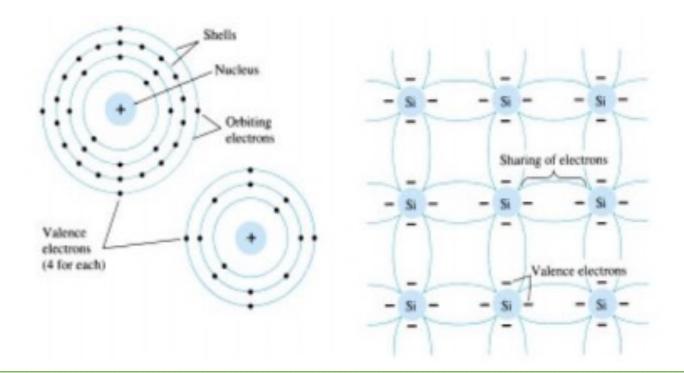


☐ Material Types



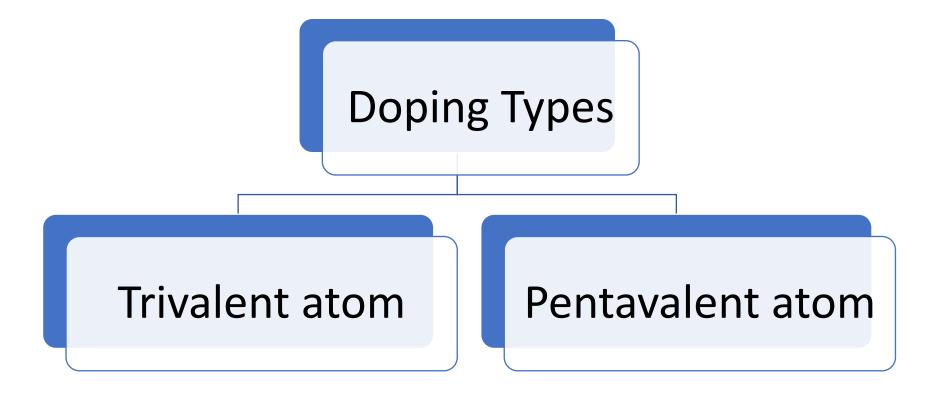
☐ Semiconductor concepts

The more popular semiconductor materials are Silicon (Si) which has 14 electrons and germanium (Ge) which has 32 electrons. All semiconductors have 4 electrons at the Fermi level.



☐ doping process

> The doping is control process by adding impurities to pure semiconductors to enhance its conductivity to electrical current.

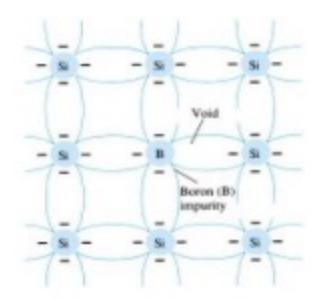


■ N-type material

This type is negative type with majority of electrons and minority of holes. The doping in this type is happened by pentavalent atom which has five electrons in the Fermi level. Four of them complete covalent bonds and still one free electron to conduct electrical current

□ P-type material

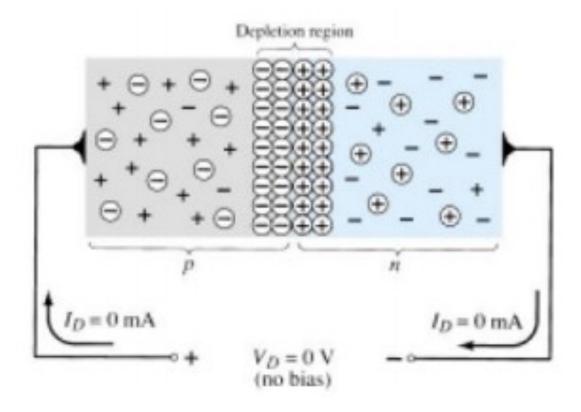
This type is positive type with majority of holes and minority of electrons. The doping in this type is happened by trivalent atom which has three electrons in the Fermi level. all of them complete covalent bonds and still one hole needs one free electron to conduct electrical current



☐ Depletion layer

- First, we will put N-type material beside P-type material. The diffusion of electrons will be happened from N-type to P-type.
- The second step is the recombination between electrons and holes to complete electron and hole pairs.
- The third step is the ionization which makes the N-type change from neutral state to positive ions (Donor's atoms) and the P-type change from neutral state to negative ions (Acceptors atoms). At the equilibrium between the attraction force between positive ions and electrons and the repulsion force between negative ions and electrons complete the depletion layer

☐ Depletion layer



☐ Forward bias

Forward bias (F.B): the connection is positive DC battery with P-type and negative DC battery with N-type generate two repulsion force between them then depletion region width is reduced then flow the electrical current I_D .

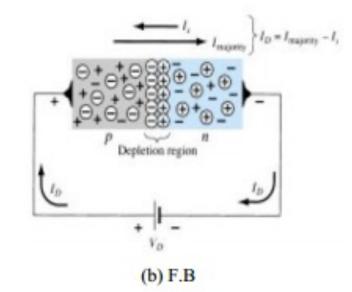
$$I_D = I_s(e^{\frac{kV_d}{T_k}} - 1) \tag{5-1}$$

where:

V_d: Voltage on diode.

k: 11,600/η with η = 1 for Ge and η = 2 for Si.

 $T_k: T_c + 273$.



☐ Reverse bias

Reverse bias (R.B): the connection is positive DC battery with N-type and negative DC battery with P-type generate two attraction force between them then depletion region width is increased then no flow the electrical current or flow small reverse saturation current I_s .

(a) R.B

